

MHS Electronics, a premier analog and mixed-signal pure-play foundry, started a collaborative research and development (R&D) program (named HIVICS) funded by the UK Department of Trade and Industry (DTI). The scope of this three-year program is DFM of high-voltage ICs on SOI for automotive applications. With the contributions of its partners Silvaco, University of Sheffield and Queen's University Belfast, the challenge of designing a low on-resistance 100V process with high packing density will be addressed by incorporating novel low thermal resistance silicided SOI substrates and integrating innovative device architectures. The project will undertake the development of advanced three-dimensional (3D) numerical simulations, improvements to existing models and enhancements to computer-aided design (CAD) tools. The new high-voltage IC technology on SOI will then be offered as a silicon foundry service.

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